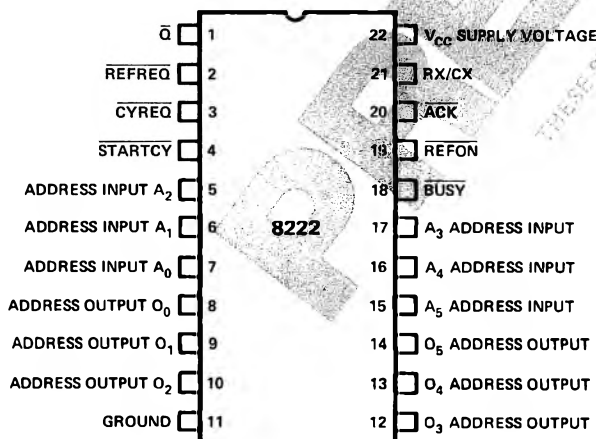# intel®

Schottky Bipolar 8222

DYNAMIC MEMORY
REFRESH CONTROLLER

- **Adjustable Refresh Request Oscillator**
- **Ideal for 8107A, 8107B 4K RAM Refresh**
- **Internal Address Multiplexer**
- **Up to 6 Row Input Addresses (64 x 64 Organization)**

The 8222 is a refresh controller for dynamic RAMs requiring row refresh of up to 6 row input addresses (or 4K bits for 64 x 64 organization). The device contains an accurate refresh timer (whose frequency can be set by an external resistor and capacitor) plus all necessary control and I/O circuitry to provide for the refresh requirements of dynamic RAMs. The chip's high performance makes it especially suitable for use with high speed N-channel RAMs like the 8107B. The 8222 is designed for large, asynchronously driven, dynamic memory systems.

PIN CONFIGURATION

Pin	Signal	Pin	Signal
1	Q̄	22	V_CC SUPPLY VOLTAGE
2	REFREQ	21	RX/CX
3	CYREQ	20	ACK
4	STARTCY	19	REFON
5	ADDRESS INPUT A_2	18	BUSY
6	ADDRESS INPUT A_1	17	A_3 ADDRESS INPUT
7	ADDRESS INPUT A_0	16	A_4 ADDRESS INPUT
8	ADDRESS OUTPUT O_0	15	A_5 ADDRESS INPUT
9	ADDRESS OUTPUT O_1	14	O_5 ADDRESS OUTPUT
10	ADDRESS OUTPUT O_2	13	O_4 ADDRESS OUTPUT
11	GROUND	12	O_3 ADDRESS OUTPUT

8222

BLOCK DIAGRAM