

## AMD Phenom II X2 555 BE (rev. C3) specifications

### General information

Type	<a href="#">CPU / Microprocessor</a>
Market segment	Desktop
Family	<a href="#">AMD Phenom II X2</a>
Model number <span>?</span>	<b>555</b>
CPU part numbers	<b>HDZ555WFK2DGM</b> is an OEM/tray microprocessor <ul style="list-style-type: none"><li><b>HDZ555WFGMBOX</b> is a boxed microprocessor with fan and heatsink</li></ul>
Stepping codes	<a href="#">AACAC AC</a> <a href="#">CACAC AC</a> <a href="#">CACDC AC</a>
Frequency <span>?</span>	<b>3200 MHz</b>
Bus speed <span>?</span>	<b>667 MHz Memory controller</b> <b>One 2000 MHz 16-bit HyperTransport link (4 GT/s)</b>
Clock multiplier <span>?</span>	<b>16</b>
Package	938-pin organic micro-PGA
Sockets	<b>Socket AM2+</b> <b>Socket AM3</b>
Fan/heatsink	CMDK8-7G52B-A1-GP
Introduction date	<a href="#">January 25, 2010</a>
End-of-Life date	December 2012
Price at introduction	\$99
<b>Architecture / Microarchitecture</b>	
Microarchitecture	K10
Platform	Dragon
Processor core <span>?</span>	<a href="#">Callisto</a>
Core stepping <span>?</span>	RB-C3
CPUID	100F43
Manufacturing process	0.045 micron silicon-on-insulator (SOI) technology
Die	258mm <sup>2</sup>
<b>Data width</b>	<b>64 bit</b>
<b>The number of CPU cores</b>	<b>2</b>
<b>The number of threads</b>	<b>2</b>
Floating Point Unit	Integrated
Level 1 cache size <span>?</span>	2 x 64 KB 2-way set associative instruction caches 2 x 64 KB 2-way set associative data caches
<b>Level 2 cache size <span>?</span></b>	<b>2 x 512 KB 16-way set associative exclusive caches</b>
<b>Level 3 cache size</b>	<b>Shared 6 MB 48-way set associative cache</b>
Cache latency	3 (L1 cache)
Multiprocessing	Uniprocessor
Extensions and Technologies	<ul style="list-style-type: none"><li>MMX instructions</li><li>Extensions to MMX</li><li>3DNow! technology</li><li>Extensions to 3DNow!</li><li>SSE / Streaming SIMD Extensions</li><li>SSE2 / Streaming SIMD Extensions 2</li><li>SSE3 / Streaming SIMD Extensions 3</li><li>SSE4a <span>?</span></li><li>AMD64 / AMD 64-bit technology <span>?</span></li><li>EVP / Enhanced Virus Protection <span>?</span></li><li>AMD-V / AMD Virtualization technology</li></ul>
Low power features	<ul style="list-style-type: none"><li>Cool'n'Quiet 3.0</li><li>CoolCore Technology <span>?</span></li><li>Dual Dynamic Power Management <span>?</span></li><li>Core C1 and C1E states</li><li>Package S0, S1, S3, S4 and S5 states</li></ul>

### Integrated peripherals / components

Integrated graphics	None
Memory controller	The number of controllers: 1 Memory channels: 2 Channel width (bits): 72 Supported memory: DDR2-1066, DDR3-1333 DIMMs per channel: up to 2 Maximum memory bandwidth (GB/s): 21.3
Other peripherals	HyperTransport 3 technology

### Electrical / Thermal parameters

V core <a href="#">?</a>	0.875V - 1.4V
Maximum operating temperature <a href="#">?</a>	55°C - 70°C
<b>Thermal Design Power <a href="#">?</a></b>	<b>80 Watt</b>

### Notes on AMD Phenom II X2 555 BE (rev. C3)

- The processor has unlocked clock multiplier