

## AMD Sempron LE-1250 (DP) specifications

### General information

Type	<a href="#">CPU / Microprocessor</a>
Market segment	Desktop
Family	<a href="#">AMD Sempron</a>
Model number <a href="#">?</a>	<a href="#">LE-1250</a>
CPU part numbers	SDH1250IAA4DP is an OEM/tray microprocessor <ul style="list-style-type: none"><li>• <a href="#">SDH1250DPBOX</a> is a boxed microprocessor with fan and heatsink</li></ul>
Stepping codes	<a href="#">NAA9G</a> <a href="#">NAABG</a>
Frequency <a href="#">?</a>	<b>2200 MHz</b>
Bus speed <a href="#">?</a>	<b>Dual-channel 333 MHz DDR2 SDRAM Memory controller</b> <b>One 800 MHz 16-bit HyperTransport link (1.6 GT/s)</b>
Package	940-pin lidded micro Pin Grid Array (micro-PGA)
Socket	<a href="#">Socket AM2</a>
End-of-Life date	Last order date is end of 4th quarter 2008

### Architecture / Microarchitecture

Microarchitecture	K8
Processor core <a href="#">?</a>	Sparta
Core stepping <a href="#">?</a>	G2
CPUID	70FF2
Manufacturing process	0.065 micron silicon-on-insulator (SOI) technology
<b>Data width</b>	<b>64 bit</b>
<b>The number of CPU cores</b>	<b>1</b>
<b>The number of threads</b>	<b>1</b>
Floating Point Unit	Integrated
Level 1 cache size <a href="#">?</a>	64 KB 2-way set associative instruction cache 64 KB 2-way set associative data cache
<b>Level 2 cache size <a href="#">?</a></b>	<b>512 KB</b>
Multiprocessing	Uniprocessor
Extensions and Technologies	<ul style="list-style-type: none"><li>• MMX instructions</li><li>• Extensions to MMX</li><li>• 3DNow! technology</li><li>• Extensions to 3DNow!</li><li>• SSE / Streaming SIMD Extensions</li><li>• SSE2 / Streaming SIMD Extensions 2</li><li>• SSE3 / Streaming SIMD Extensions 3</li><li>• AMD64 / AMD 64-bit technology <a href="#">?</a></li><li>• EVP / Enhanced Virus Protection <a href="#">?</a></li></ul>

### Integrated peripherals / components

Integrated graphics	None
Memory controller	The number of controllers: 1 Memory channels: 2 Supported memory: DDR2-667 <sup>[1]</sup> Maximum memory bandwidth (GB/s): 10.7 <sup>[1]</sup>
Other peripherals	HyperTransport 1.0 technology

### Electrical / Thermal parameters

V core <a href="#">?</a>	1.2V / 1.35V / 1.4V
Maximum operating temperature <a href="#">?</a>	75°C
<b>Thermal Design Power <a href="#">?</a></b>	<b>45 Watt</b>

### Notes on AMD Sempron LE-1250 (DP)

- Processor supports x86-64 (64-bit) mode
- One source claims the CPU supports DDR2-800 memory

[1] - Information is not confirmed

